

FIG. 1  
(PRIOR ART)

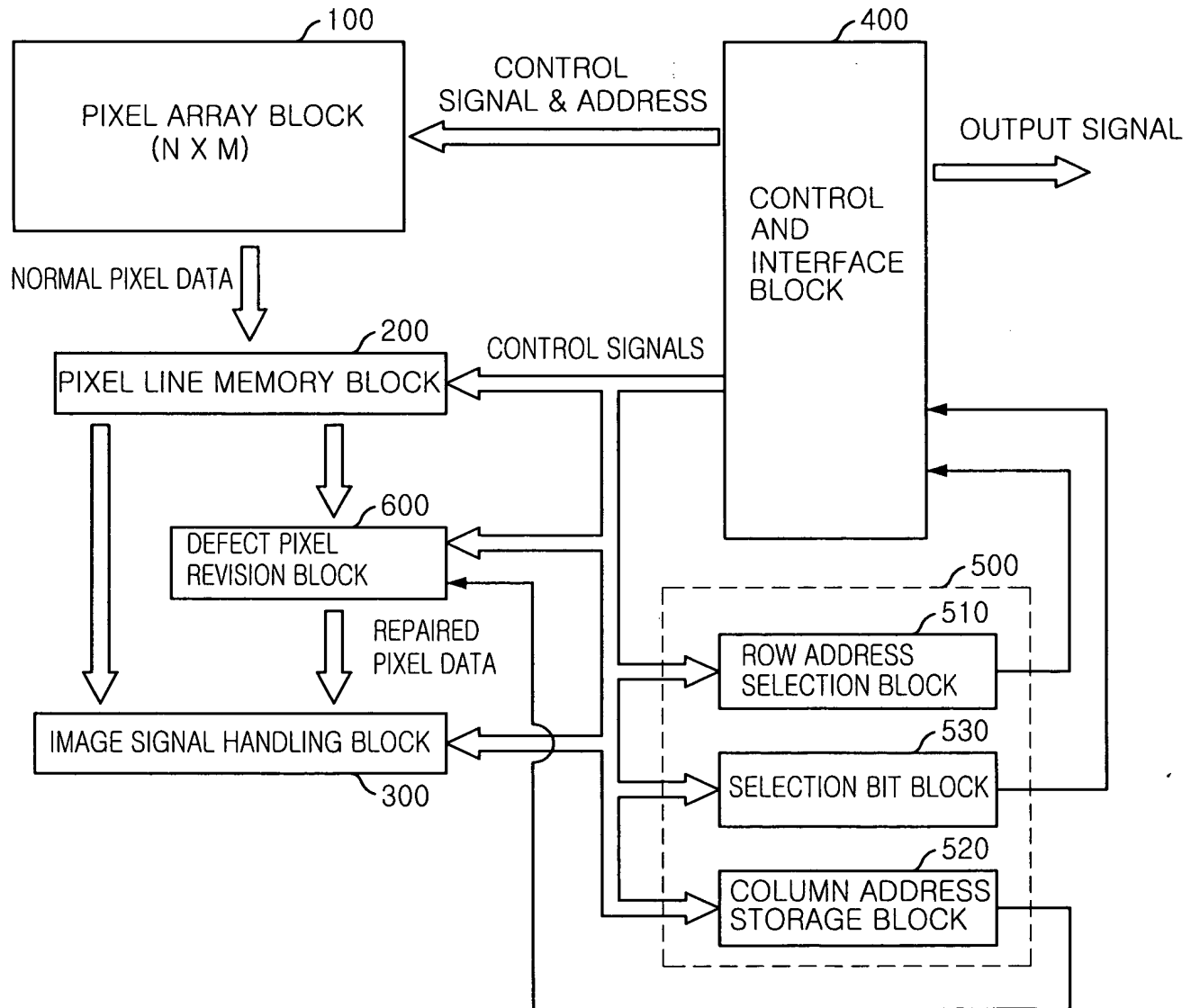


FIG. 2

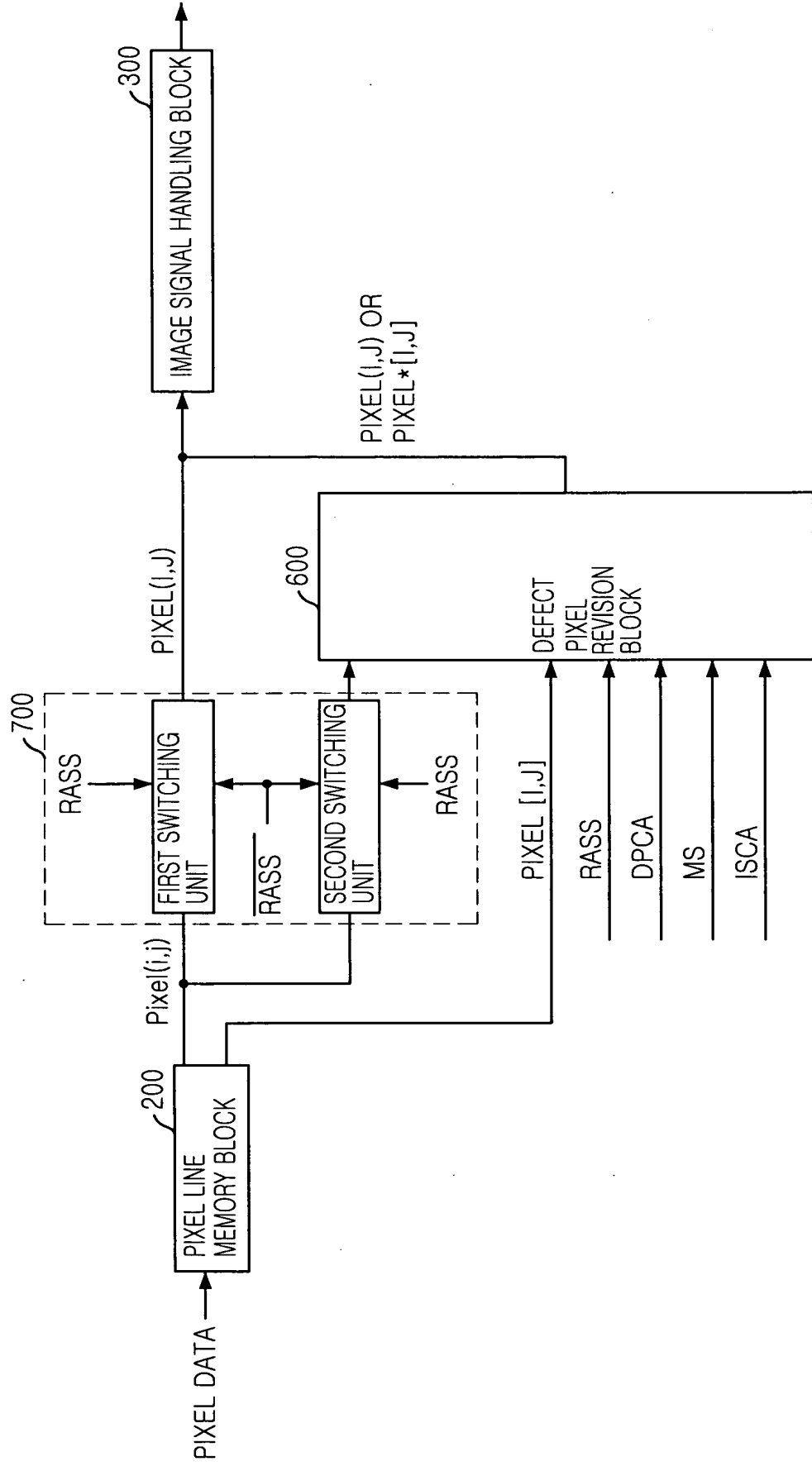


FIG. 3

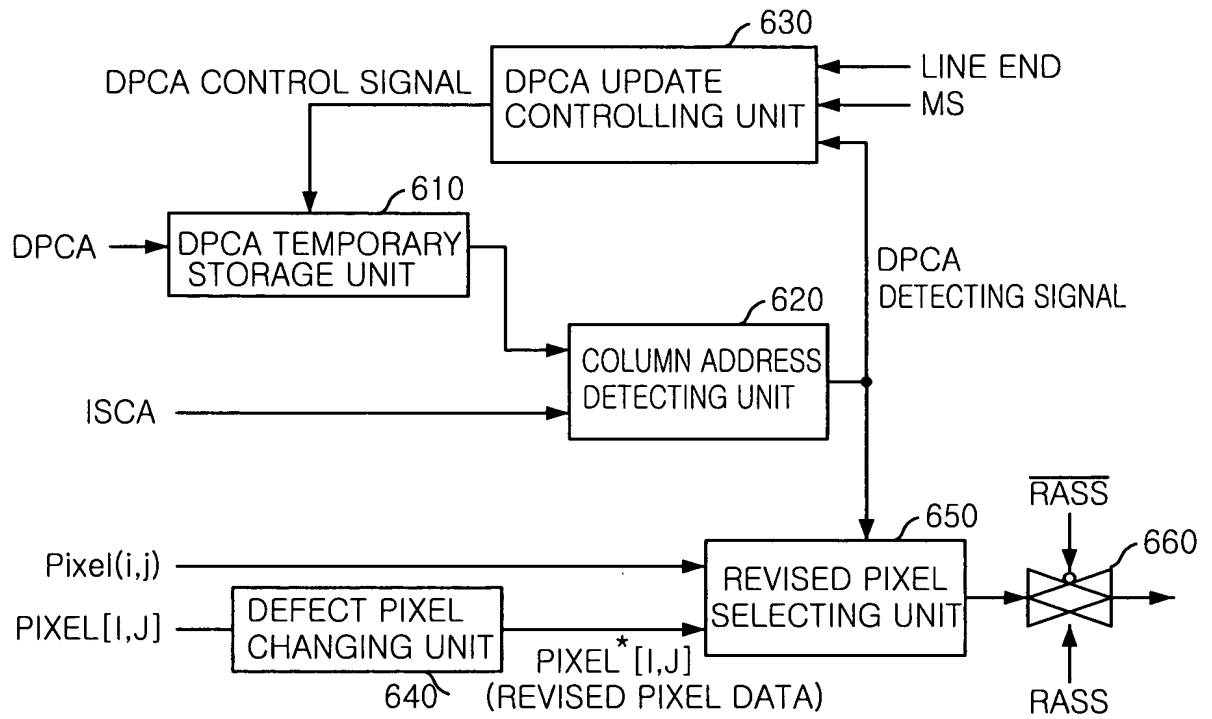


FIG. 4

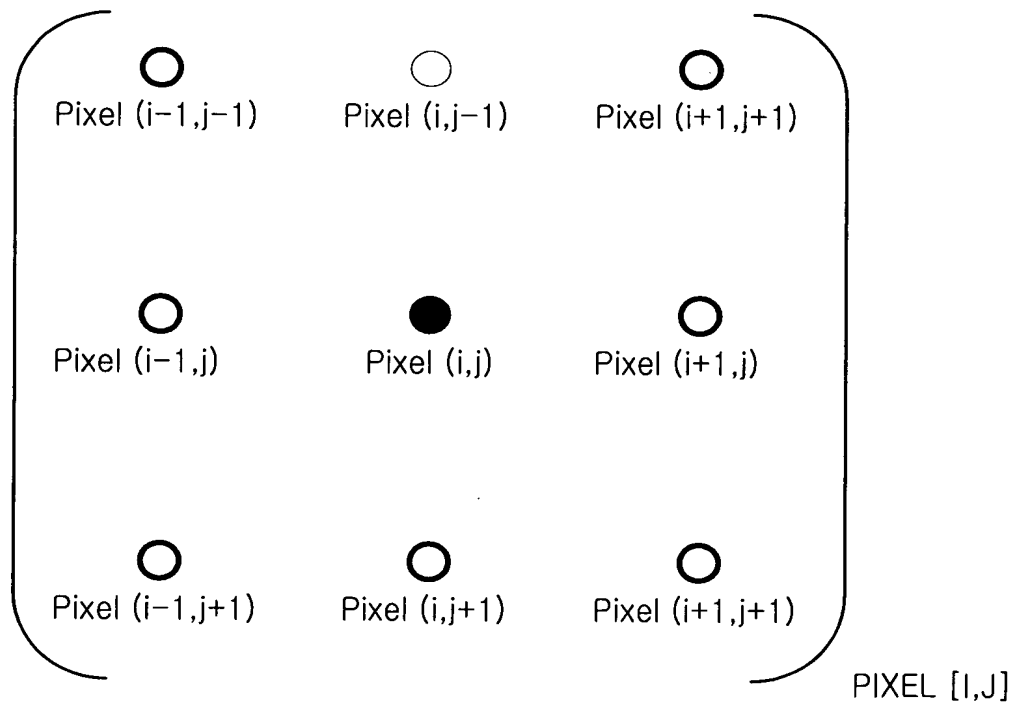


FIG. 5

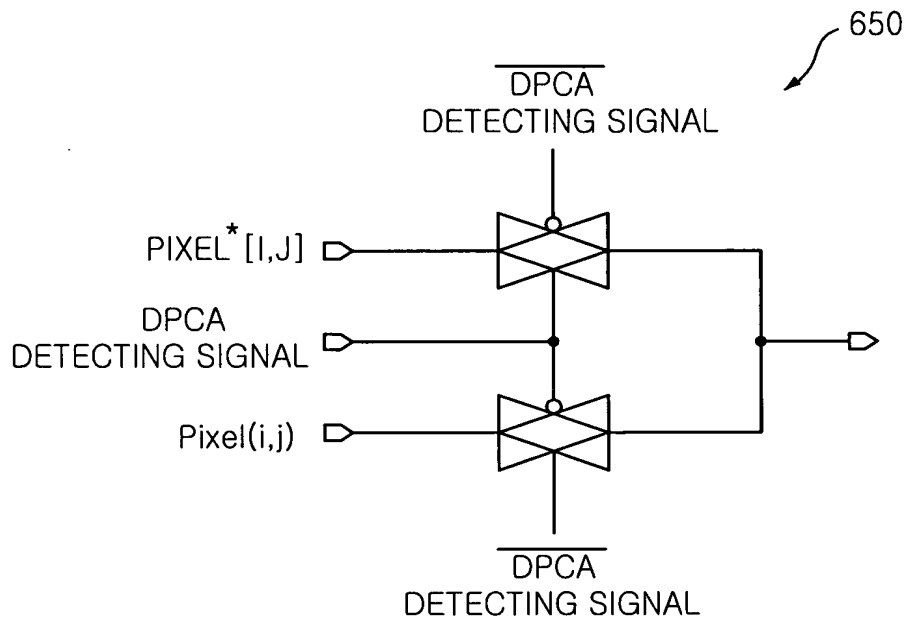


FIG. 6

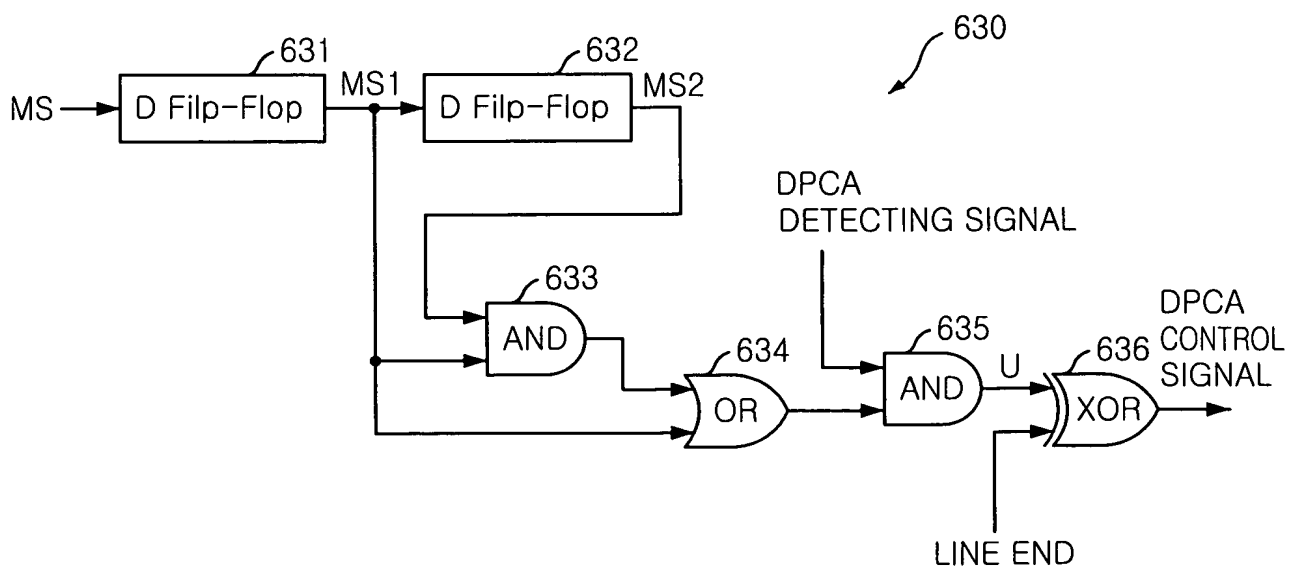


FIG. 7A

MS1	MS1	DPCA DETECTING SIGNAL	U	DPCA CONTROL SIGNAL
X	X	0	0 (HOLD)	0 (HOLD)
0	0	1	0 (HOLD)	0 (HOLD)
0	1	1	0 (HOLD)	
1	0	1	1 (UPDATE)	1 (UPDATE)
1	1	1	1 (UPDATE)	

FIG. 7B

LINE END	U	DPCA CONTROL SIGNAL
1	0	1 (UPDATE)
	1	0 (HOLD)

FIG. 8

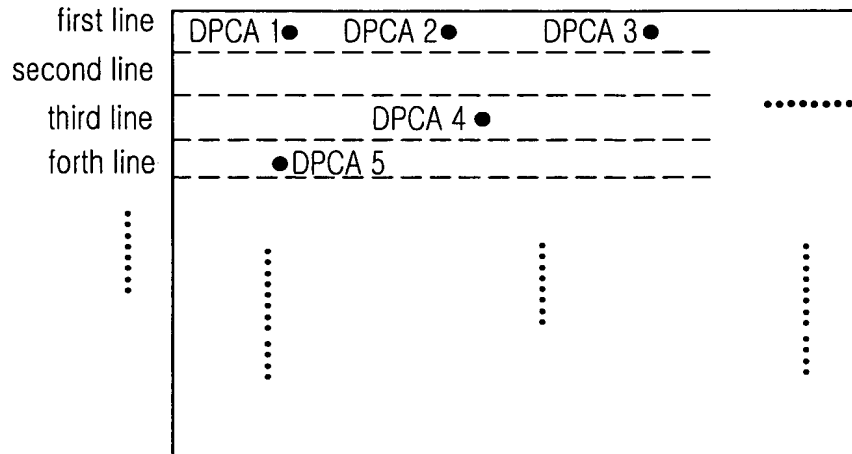


FIG. 9

